

# The Quest for 100dB Isolation in Mixed-Signal ASICs

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## Summary

- The Problem: **Isolation**
- Nature of the problem: **Not simple**
- Many ways to solve the problem
- One solution: **Faraday Cages**

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### ■ The Problem: **Isolation**

- We need to provide good isolation, sometimes exceeding 100dB, among circuits that co-exist on a single die.
- Thermal and magnetic isolation are not irrelevant, but they are beyond the scope of this presentation.

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### ■ Nature of the problem: **Not Simple**

- Ultimately, there are nontrivial tradeoffs and it remains necessary to apply good engineering judgement.

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### ■ Many ways to solve the problem:

- Dis-integrate
- Relax spec
- Exploit cyclostationarity (Move the noise to another timeslot)
- Low- $\rho$  substrate with *zero* impedance ground
- High- $\rho$  substrate with integrated Faraday cages

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### ■ One Solution: **Faraday Cages**

- Adopt new circuit and layout techniques that allow us to integrate Faraday cages around each element that requires isolation.

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## Concepts

- Substrate Noise
- Faraday Cage
- Cyclostationary Noise

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## Substrate Noise



<--- Useful reference (Kluwer, 2001)

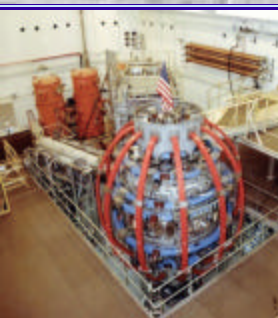
Substrates are distributed RC networks (unless you are unlucky enough to get latchup or other disasters that involve active devices).

The RC networks are complex, hard to characterize, and generally intractable.

Because they couple circuits that were supposed to be isolated, they introduce correlated or uncorrelated noise.

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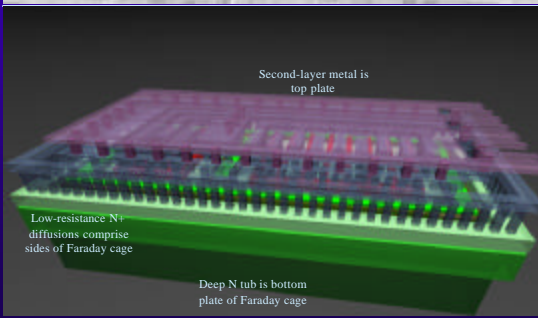
## Faraday Cage



**Faraday Shield (or Faraday Cage):** An electrostatic shield named after its inventor Michael Faraday. It is made by placing conductive material (often aluminum or copper) around some device and connecting that material to ground. The better the conductor (in other words, the less resistance it has) the better shield it will be. The braided wire around the conductor(s) in audio cable is an example of a Faraday Shield. They are also used in transformer designs (to prevent capacitance between the primary and secondary windings), and the most sensitive electronics on circuit boards, in which case they are often deployed as a hard aluminum cage around said components.

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## Faraday Cage, containing oscillator




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## Cyclostationary Noise

- Cyclostationarity
  - Definition: having a periodically time-varying power spectrum
- Example (from Hajimiri and Lee):
  - "... due to the periodically changing operation points of the active devices in the oscillator, many of the noise sources have a periodically changing power spectrum; they are cyclostationary."

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## Cyclostationary Noise



Hajimiri and Lee (Kluwer, 1999) is an excellent reference. Although this concept is most commonly applied to oscillators, it pertains to a wide class of circuits.

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## Isolation Requirements

- **Monolithic radios (GPS, Bluetooth)**
  - Microvolt input signals
  - Large amount of on-chip logic
- **MEMs and MEMs drivers**
  - 100v output swing is common
  - Analog processing on same chip
  - On-chip sensors with very low signal amplitude
- **Ultrasound signal processors**
  - Input signal magnitude is swept from volts to millivolts
  - Required SNR exceeds 100dB

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## Magnitude of the Problem

- (Xu, 2001). Substrate noise is ~ -10 dBm. This is several orders of magnitude higher than the -130 dBm signal present at the input of her monolithic GPS receiver chip.

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## CMOS Processes

- Bulk, high- $\rho$  substrate
- Epi, on low- $\rho$  substrate
- High- $\rho$  substrate with low- $\rho$  buried layer

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## CMOS Processes

- Bulk, high- $\rho$ 
  - ubiquitous (TSMC, etc.)

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## CMOS Processes

- Epi, on low- $\rho$  substrate
  - widely available as option (TSMC, etc.)
  - *de rigueur* in some high performance variants (eg. IBM, ST)

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## CMOS Processes

- High- $\rho$  substrate with low- $\rho$  buried layer
  - Growing availability, driven by
    - RF BICMOS
      - IBM
      - ST
      - XFAB
    - HV CMOS
      - Alcatel
      - XFAB

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## Case Histories: Low- $\rho$ Substrate

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## Badaroglu

IMEC - 02005 - 0004 Workshop on Substrate Noise-Coupling in Mixed-Signal ICs, IMEC, Belgium, Sep. 2001

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## Badaroglu

IMEC - 02005 - 0004 Workshop on Substrate Noise-Coupling in Mixed-Signal ICs, IMEC, Belgium, Sep. 2001

[Hoflinger et al. IEEE JSSC 2006]

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## Our $\Sigma\Delta$ converter, low- $\rho$ substrate

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## $\Sigma\Delta$ converter, low- $\rho$ substrate

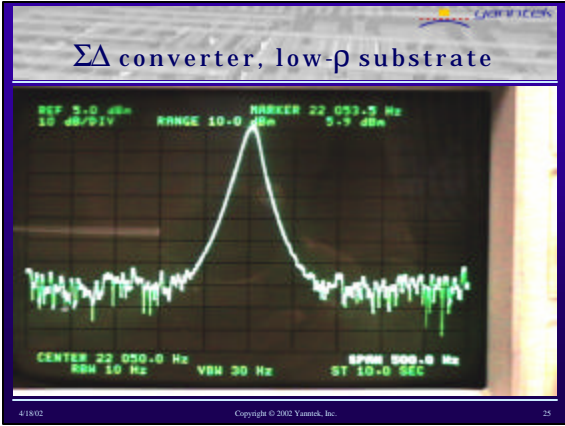
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        PWR_OUT_N[PWR_OUT-] --> DDD
        REF[REFERENCE] --> SIF[SINC FILTERS]
        SIF --> DDD
        SIF --> PAMP[PREAMP]
        PAMP --> SIF
        PAMP --> SAM[Sigma-Delta Analog Modulator]
        SAM --> SIF
        SAM --> POSP[POSP]
        PAMP --> POSP
        PAMP --> DDD
        SAM --> DDD
        PAMP --> SIF
        SIF --> PAMP
        SIF --> SAM
        SIF --> POSP
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        SIF --> POSP
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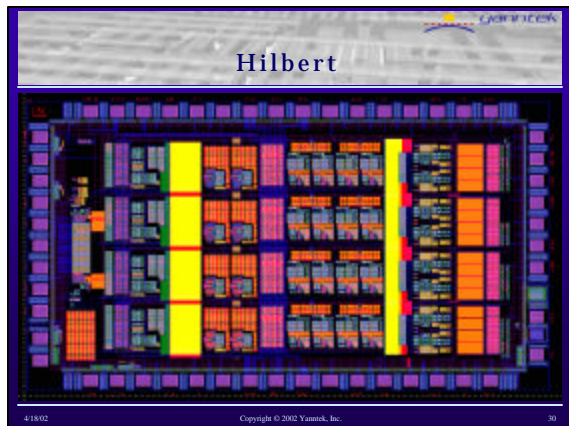
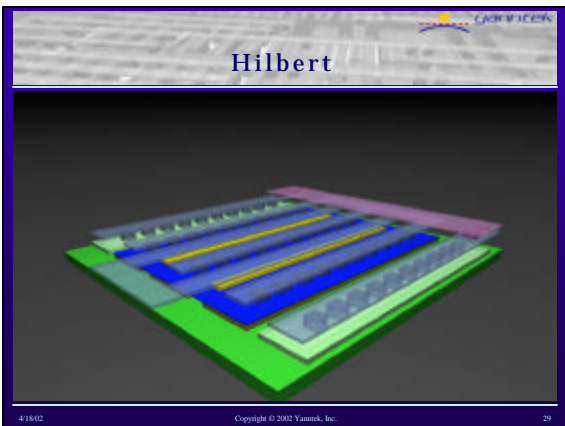
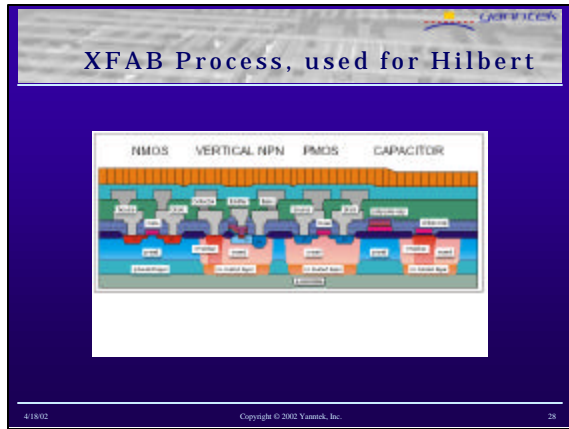
## $\Sigma\Delta$ converter, low- $\rho$ substrate

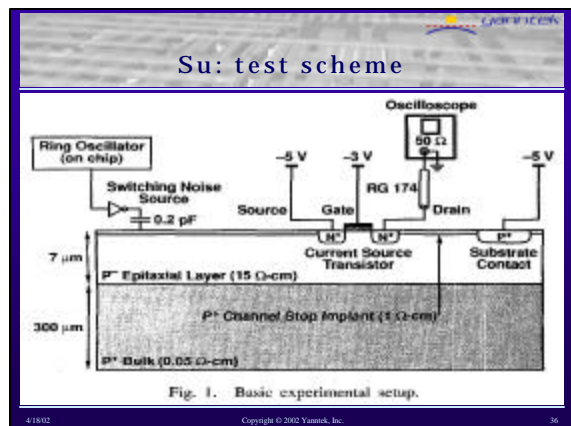
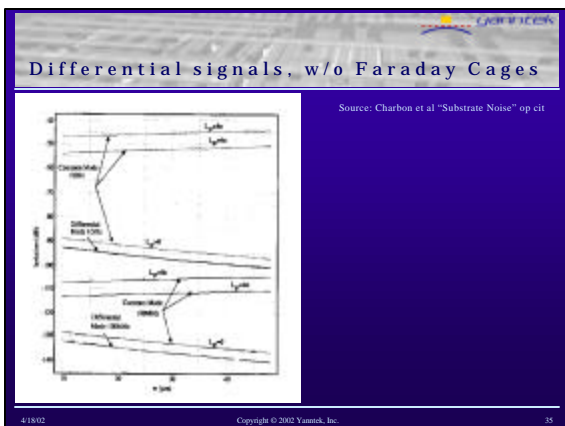
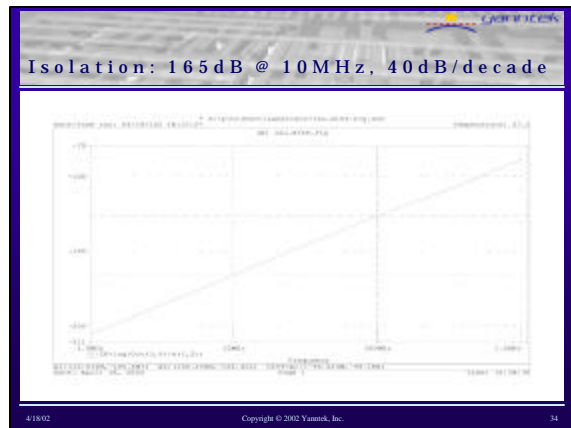
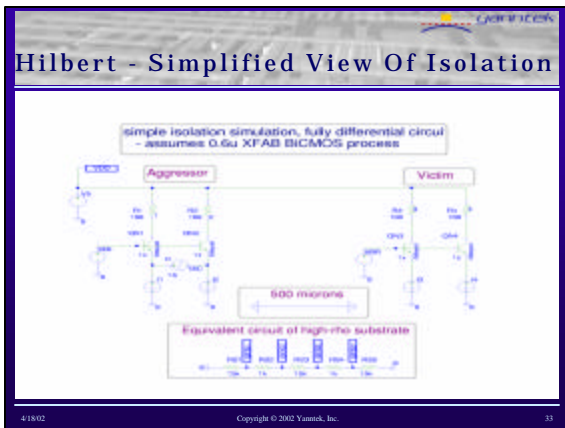
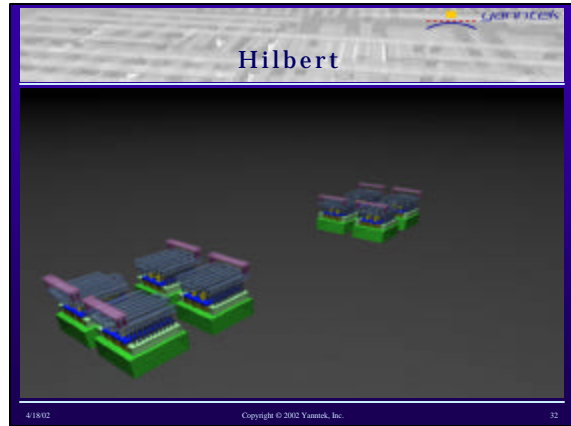
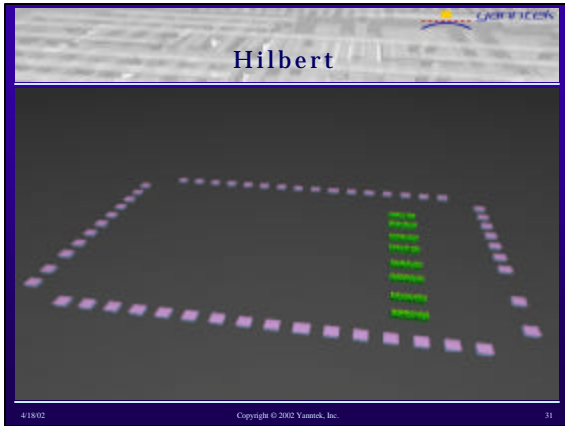
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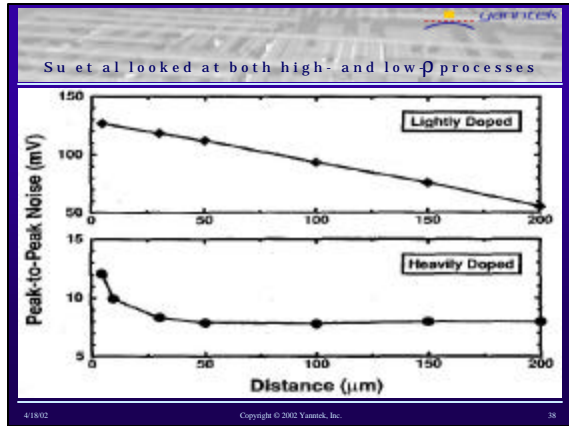
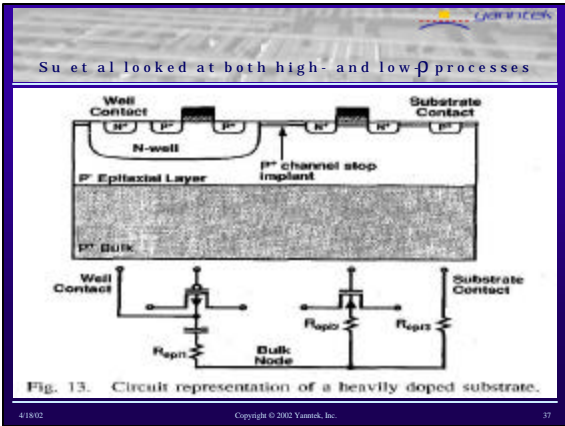


- ### Case Histories: High-ρ Substrate
- Yantrak: Ultrasound chip (Hilbert)
  - Su: test chip, at Stanford
    - Su, et al "Experimental Results and Modeling Techniques in Mixed-Signal Integrated Circuits" JSSC v 28 no 4 April 93 p420+.
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- ### Case Histories: High-ρ Substrate
- Hilbert is a TGA for an ultrasound system
  - Built in XFAB 0.6u BICMOS
  - Modest isolation requirements (< 100dB)
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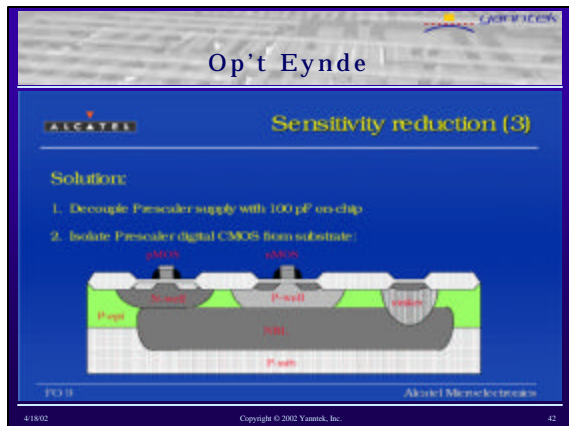
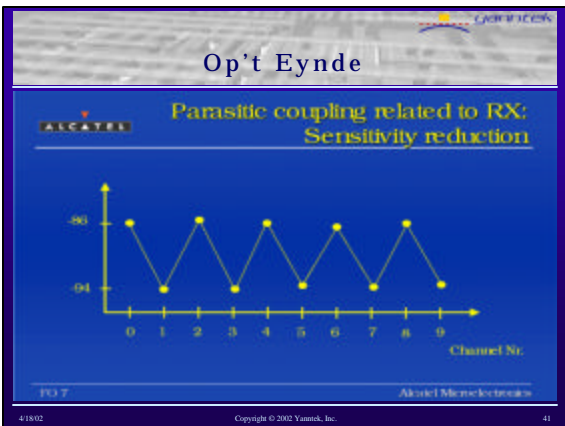


### Case Histories: Integrated Faraday shield

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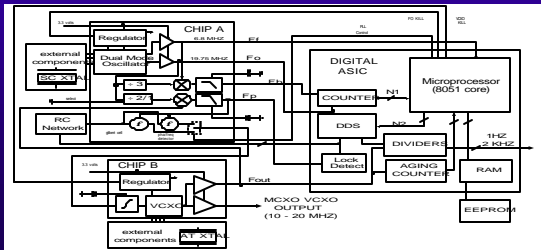
### Op't Eynde

**ALCATEL** Degree of Integration



## Our Immediate Problem

- Extreme-stability clock  
achieve atomic-clock performance at smaller power and size



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## Conclusion

- > 100dB isolation is readily achieved, under some circumstances.
- Faraday cages are effective, and they can be used to suppress noise at either the aggressor or victim location

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